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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/072,417  
Filing Date ..... February 5, 2002  
Inventor ..... Joseph M. Brand  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2826  
Examiner ..... Pershelle L. Greene  
Attorney's Docket No. .... MI22-1939  
Title: Integrated Circuit Device

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**


References -See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449, a copy of which are attached. No admission is made regarding whether the submitted reference is prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 1-21-03

By:   
D. Brent Kenady  
Reg. No. 40,045

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PATENT &amp; TRADEMARK OFFICE

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
M122-1939SERIAL NO.  
10/072,417LIST OF ART CITED BY APPLICANT  
(Use several sheets if necessary)APPLICANT  
Joseph M. BrandFILING DATE  
February 5, 2002GROUP  
2826

## U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,097,317	03/17/92	Fujimoto et al.	357	72	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						

## FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AM							
	AN							
	AO							
	AP							
	AQ							

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

	AR		
	AS		

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.